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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM 10033007	FILING DATE 12/28/2001	CLASS 764 324	SUBCLASS 727	GAU 2/33 2858	EXAMINER <i>Healey</i>
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****APPLICANTS:** McAdams Mark; *RSD* *TRIMMING*

****CONTINUING DATA VERIFIED:**

**** FOREIGN APPLICATIONS VERIFIED:**

PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO G056
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no	
Verified and Acknowledged Examiners's initials	
TITLE : Testing of circuit modules embedded in an integrated circuit	

U.S. DEPT. OF COM. / PAT. & TM. PTO. - 436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
ISSUE FEE Amount Due Date Paid		Assistant Examiner	
		Primary Examiner	
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	
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